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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

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TITEL: Using transition time checks to determine noise problems on signal lines of an integrated circuit,	Foreign priority of 35 USC 119 con Verified and Act	iaimed ditions met nowledged Examiners's	ע ₪ ע ₪ intials M	es Inc res Inc and I	ms	1001	1030-1		-

NOTICE OF ALLOWANCE MAILED	· · · · · · · · · · · · · · · · · · ·	CLAIMS ALLOWED				
	Assing	Total Claims Print Claim for O.G				
Amount Due Date Paid		DRAWING Sheets Drwg. Figs.Drwg. Print Fig.				
	Primary Examiner					
TERMINAL	PREPARED FOR ISSUE	Application Examiner				
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